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Complete if Known

Application Number	10/728451
Filing Date	December 5, 2003
First Named Inventor	Scheuerlein, Roy
Group Art Unit	Unknown
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: MA-112

US PATENT DOCUMENTS

Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
NDR	A1	10/335078		Scheuerlein et al.			12/31/2002
NDR	A2	10/611246		Cleeves			06/30/2003
NDR	A3	5835396	11/10/1998	Zhang			
NDR	A4	6034882	03/07/2000	Johnson et al.			
NDR	A5	6185121	02/06/2001	O'Neill			
NDR	A6	US20020079553A1	06/27/2002	Cleeves			

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
NDR	A7	SCHEUERLEIN, ROY.E. , et al. , "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", <u>2000 IEEE International Solid-State Circuits Conference: ISSCC 2000, Feb 8, 2000: Emerging Memory and Device Technologies/Paper TA 7.2, (2/8/2000), 128-129</u>	

EXAMINER *[Signature]*

DATE CONSIDERED 9/26/05

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional) * Applicant is to place a check mark here if English language Translation is attached